



**Steven J. Hillenius**  
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Steven Hillenius is Executive Vice President of Semiconductor Research Corporation & Executive Director of GRC, prior to coming to SRC he was Director, Integrated Circuit Device Technology Department at Agere Systems where he was responsible for semiconductor research and technology development at Bell Laboratories and Agere systems. He worked for Bell Laboratories/Agere Systems from 1981 until 2006. He was an Assistant Professor of

Physics at the University of Virginia from 1978-81, where his research involved low temperature solid-state physics.

Dr. Hillenius has published over 70 articles on semiconductor devices and processing. In 1996 he was elected to the grade of IEEE Fellow with the following citation: "For contributions to the field of solid-state technology and its applications to integrated circuits". He has also been involved with the planning and the road mapping for the semiconductor industry for the last decade through his involvement with the Semiconductor industry association International Technology Roadmap for Semiconductors (ITRS), the SIA Technology Strategy Committee and currently in his position at SRC. He holds several key patents as either inventor or co-inventor in the area of microelectronics. Two of these patents were recognized by being the choice for the 2005 Agere Innovation Award, given to the Agere Systems inventor of the most commercially significant patent and the AT&T Patent Recognition Award in 1992, presented for that year's most commercially significant patent.

Dr. Hillenius is a fellow of the IEEE, and a member of Sigma Xi, AAAS and APS. He has served in many volunteer and elected positions within the IEEE including president of the Electron Devices Society, Member of the Education Advisory Board, member of the Technical Advisory Board the IEEE Board of Directors and on the board of IEEE USA. He received a Ph.D. in physics from the University of Virginia, Charlottesville, VA, in 1979.